

| Design Gateway Co., Ltd. Company Overview | | | Date: 03/20/2023 | |
|---|-----------------------------------|---------------------------|--|--|
| | | | www.design-gateway.com | |
| Partner Details | Partner Tier: | | Elite Certified | |
| | Headquarte | rs Location: | 89/13 Amornpan 205 Tower1, 11th floor, Nathong Alley, Din Daeng,Bangkok,,Thailand | |
| > | Kilinx Alliance Pa | artner Since: | 3/4/2013 | |
| | | | | |
| Key Contact Info | Phunjapa Ru | - | | |
| | phunjapa@d | esign-gateway. | com | |
| | APAC. Centr | al and South Aı | merica. | |
| Regions Served | Eastern Europe and Russia, Japan, | | | |
| | Middle East, | North America | Central | |
| | | Design | | |
| Supported Product Types | Yes | Services | No Embedded Products | |
| | Yes | IP | No EDA Tools | |
| | Yes | Boards | | |
| Frankraskin v Complete Trusco | V | O I D | on Control | |
| Engineering Service Types | Yes Yes | General Purp | ose Services upport of Products Sold Preferred | |
| | | | ding to Contract | |
| | Yes | Manufacturing | | |
| Maximum | Service Scope: | Chip level FP design | GA / ASIC | |
| Engagement Models Supported | Yes | SOW-Based | d Fixed Bid Projects Preferred | |
| gg | Yes | Time and M | • | |
| | At my location | Captive Res Customer P | sources at Partner or remises | |
| Xilinx Design Tool Experience | Yes | Vivado | No SDSoC | |
| Allilix Design 1001 Experience | No | HLx | No SDAccel | |
| - | - 10 | | . 32.1883. | |
| Industry Certifications Achieved | | | | |
| | . 05 500 | | | |
| Industry Certification Services Offered | CE, FCC | | | |
| Government Clearances | _ | | | |





| Design Gatewa | y Co., Ltd. Segment and A | Application Focus | |
|-----------------|---------------------------|--|---|
| Primary Focus | Defense/Aerospace | No Focus | Homeland Security Electronic Warfare ISR Space Missiles & Munitions Military Avionics Commercial Avionics SatComm Wireless Milcom Other A&D |
| Secondary Focus | Audio, Video, Broadcast | No Focus | Encoders & Decoders Monitors & Projection Networking & Converters Audio Systems Video Processing Cards Video Conferencing Routers and Switches Pro Cameras & Camcorders Other Broadcast |
| Secondary Focus | Automotive & Transport | No Focus No Focus No Focus No Focus No Focus | Driver Assist - Camera Driver Assist Radar/LIDAR Driver Info/Infotainment Automated Driving Other Automotive |
| No Focus | Consumer | No Focus No Focus No Focus No Focus | Digital Display Drones Multifunction Printers Other Consumer |
| Primary Focus | Datacenter | No Focus No Focus No Focus | Storage Compute Networking |
| Primary Focus | Industrial, Medical | No Focus | Motor Control Industrial Networking Surveillance Machine Vision Medical Imaging Medical / Clinical Other ISM |



Design Gateway Co., Ltd. Segment and Application Focus (continued)

| Primary Focus | Test & Measurement | No Focus No Focus No Focus No Focus No Focus No Focus | Simulation / Emulation T & M Instrumentation Semiconductor Test Wired Communication Test Wireless Communications Test Other Test & Measurement |
|---------------|---------------------------|--|---|
| Primary Focus | Wired Comms & Networks | No Focus | Access Equipment Long Haul/Transport Wired Mobile Backhaul OTN Enterprise Equipment Ethernet Interlaken Connectivity Packet Processing Other Wired Communications |
| No Focus | Wireless Comms & Networks | No Focus No Focus No Focus No Focus No Focus | Baseband Wireless Backhaul Connectivity & Switching Radio Other Wireless |



| Design Competer System Architecture | 110100 | No Focus | |
|--|--|---------------|--|
| System Architecture | | No Focus | |
| RF and Analog Design | 1 | No Focus | |
| FPGA Design and Inte | egration | Primary Focus | Design Gateway Co., Ltd. specializes in IP cores for high performance and low resources usage solutions in data storage, networking and security fields on FPGA such as NVMe, SATA, TCP/IP, UDP/IP, EMAC, AES, SHA and tCAM. |
| FPGA Timing Closure | | No Focus | |
| FPGA Design Optimiz | ation | Primary Focus | Customization support for DG's IP Core |
| Signal Integrity Designand Analysis | n | No Focus | |
| | Inhouse or Subcontracted: Typical Serial Speed: | 0 | |
| PCB Design & Layout | | Primary Focus | Design schematic, PCB, assembly then test and debug board to prove interface correctly. |
| | Inhouse or Subcontracted: | | |
| Industrial and Mechar | nical Design | No Focus | |
| | Inhouse or Subcontracted: | | |
| Industry Certification Compliance Testing | | No Focus | |
| Quality and Reliability | Inhouse or Subcontracted: | No Focus | |
| Testing | Inhouse or Subcontracted: | | |
| Contract Manufacturi | | No Focus | |
| | Inhouse or Subcontracted: | | |
| | Max Quantities Supported: | | |
| Digital Signal Process | _ | No Focus | |
| Embedded Processor | Experience: | No Focus | |
| Embedded i 10063301 | | . 10 1 0003 | |
| Embedded Porting / | Experience: | No Focus | |
| BSP Extension | | | |
| | d RTOS Design Experience: | | |
| Embedded Application | | No Focus | |



| Development | |
|--|--|
| Image Sensor Fusion | |
| Homo or Heterogeneous: | |
| Sensor Types Design Experience: | |
| Computer Vision | |
| Machine Learning Framework Experience: Network Model Experience: | |
| | |